

**High-Bandwidth Memory Interface (SpringerBriefs In  
Electrical And Computer Engineering)  
By Chulwoo Kim;Hyun-Woo Lee;Junyoung Song**

**Lee Hyun- woo | Refer ncias na Internet | -**

Lee Hyun-woo (Hangul: ; born March 23, 1993) is a South Korean actor. Though the song has been holding strong on the charts for several weeks,

<http://pt.cyclopaedia.net/wiki/Lee-Hyun-woo>

**Patent US20150078057 - High bandwidth memory -**

Several generations of high bandwidth DRAM-type memory devices an improved high bandwidth chip-to-chip interface for Service - About Google Patents

<http://www.google.com/patents/US20150078057>

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High-Bandwidth Memory Interface (SpringerBriefs in Electrical and Computer Engineering) Chulwoo Kim, Hyun-Woo Lee, Junyoung Song.

<http://www.amazon.co.jp/gp/bestsellers/english-books/98593011>

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High-Bandwidth Memory Interface SpringerBriefs in Electrical and Computer Engineering: Amazon.es: Chulwoo Kim, Hyun-Woo Lee, Junyoung Song: Libros en idiomas extranjeros

<http://www.amazon.es/High-Bandwidth-Interface-SpringerBriefs-Electrical-Engineering/dp/3319023802>

### **memory interface | ammar kamoona - Academia.edu -**

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**High Bandwidth Memory - HotHardware -**

AMD has publicly disclosed details regarding the revolutionary High Bandwidth  
Memory (HBM) interface that will be High Bandwidth Memory is designed to  
address

<http://hothardware.com/news/amd-details-high-bandwidth-memory-hbm-interface-technology>

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**AMD discussing FuryX GPU high bandwidth memory** - -

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## **DEVELOPING HIGH-SPEED MEMORY INTERFACES: THE -**

DEVELOPING HIGH-SPEED MEMORY INTERFACES: design high-speed memory interface solutions. high bandwidth memory devices with a high

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High Bandwidth Memory (HBM) is a high-performance RAM interface for 3D-stacked The competing technology is the incompatible Hybrid Memory Cube interface from

[http://en.wikipedia.org/wiki/High\\_Bandwidth\\_Memory](http://en.wikipedia.org/wiki/High_Bandwidth_Memory)

## **3D-ICs | JEDEC -**

interface and packaging standards for many generations from the very beginning of semiconductor memory including DRAM, HIGH BANDWIDTH MEMORY (HBM) DRAM: JESD235

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